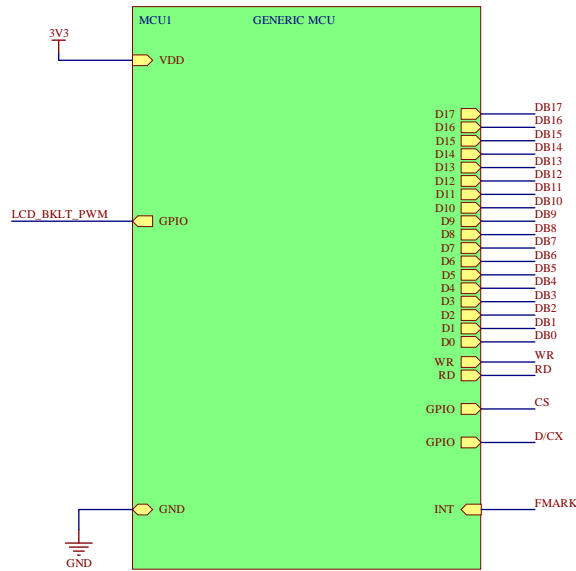
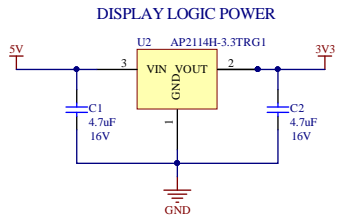


DT018ATFT MCU Reference Design

(MCU PARALLEL 18-BIT IMAGE AND CONFIGURATION DATA)

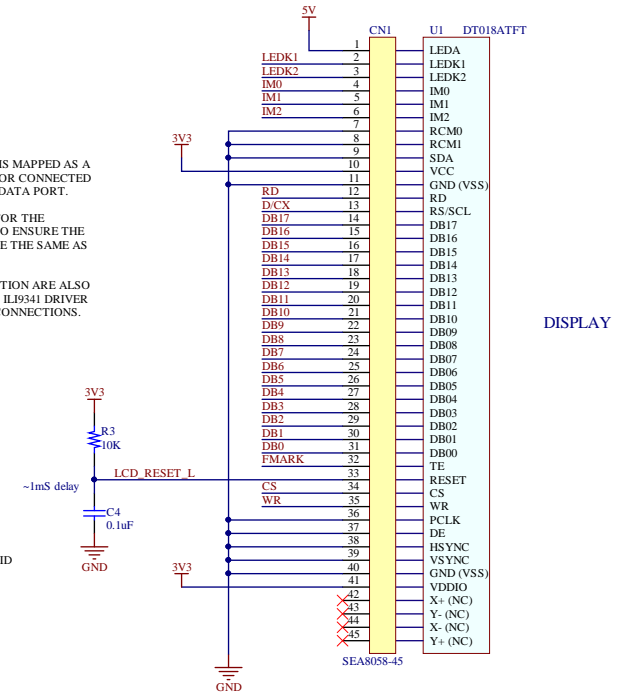


IN MCU MODE, THE DISPLAY IS MAPPED AS A PARALLEL MEMORY DEVICE OR CONNECTED TO A DEDICATED PARALLEL DATA PORT.

CONSULT THE DATA SHEET FOR THE PARTICULAR MCU CHOSEN TO ENSURE THE LCD DATA CONNECTIONS ARE THE SAME AS THIS EXAMPLE.

16-BIT AND 8-BIT MCU OPERATION ARE ALSO POSSIBLE. SEE THE MCU AND IL9341 DRIVER DATA SHEETS FOR PROPER CONNECTIONS.

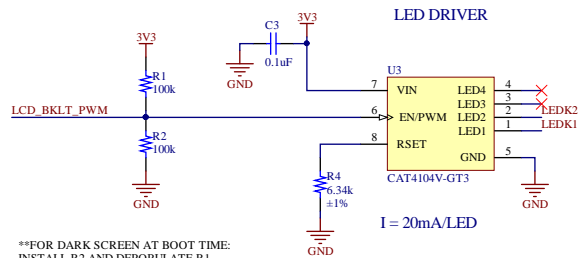
FMARK SIGNAL INDICATES THE BEGINNING OF A NEW IMAGE FRAME. IT CAN BE USED TO AVOID IMAGE TEARING.



FOR MCU MODE: RCM0 = 0, RCM1 = 0.


LOGIC 1 = VDD (3.3V)
LOGIC 0 = GND

CHIP SELECT (CS) SIGNAL IS USED TO SELECT DISPLAY IF DATA BUS IS SHARED WITH OTHER DISPLAYS OR MEMORY DEVICES. CS MAY BE TIED TO GND IF THERE ARE NO OTHER DISPLAYS OR DEVICES ON THE BUS.



**FOR DARK SCREEN AT BOOT TIME:
INSTALL R2 AND DEPOPULATE R1.
**FOR NORMAL SCREEN AT BOOT TIME:
INSTALL R1 AND DEPOPULATE R2.

I = 20mA/LED

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